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(54) Receiver comprising intermodulation interference detection means

(57) Receiver comprising analog signal processing means (1-9) and intermodulation interference detection means (10-15) for detecting intermodulation interference occurring in said analog signal processing means (1-9). According to the invention the gain of said analog signal processing means (1-9) is modulated with a gain modulation signal. The amplitude variation of the output signal of the analog signal processing means (1-9) due to said gain modulation signal is detected and supplied

to a first input of a correlator (12). The gain modulation signal is also supplied to a second input of the correlator (12). The correlator (12) causes an intermodulation interference indicative signal to occur at a detection of differences in relative amplitude variations of the output signal of level detecting means (13,14) at the first terminal of the intermodulation interference detection means (10-15) on the one hand and the gain modulation signal at the second terminal of the intermodulation interference detection means (10-15) on the other hand.

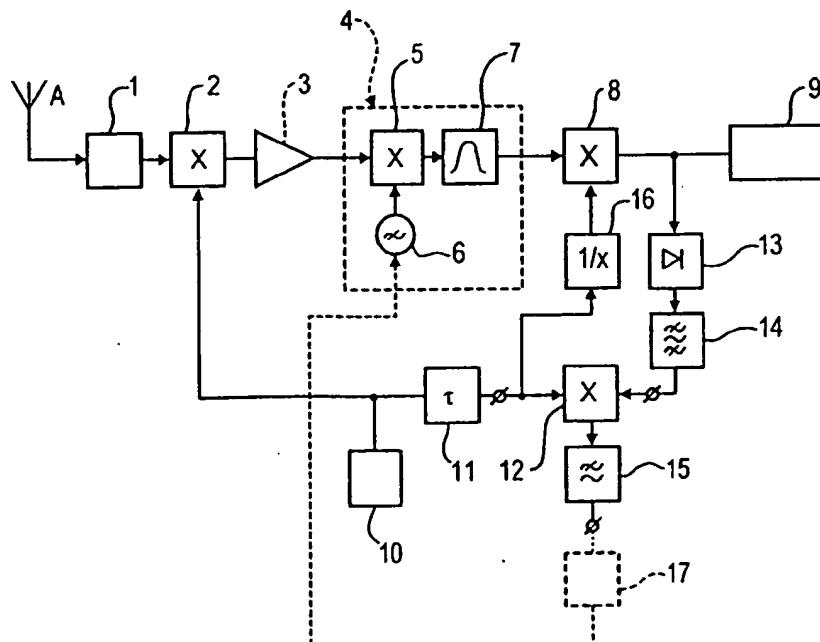


Fig. 3
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Description

[0001] The invention relates to a receiver comprising analog signal processing means and intermodulation interference detection means for detecting intermodulation interference occurring in said analog signal processing means.

[0002] Such a receiver is on itself known from US patent 4,654,884. The radio receiver disclosed therein, comprises analog signal processing means such as a tuner and an IF amplifier, followed by intermodulation interference detection means included in an automatic gain control circuit. In the intermodulation interference detection means, intermodulation interference is detected by comparing the signal level within the bandwidth of a wanted radio-channel with that of radio-channels deviating from the wanted radio-channel by frequency distances allowing intermodulation interferences to occur within the wanted radio-channel. The frequencies of those radio-channels, hereinafter indicated as interfering radio-channels, are derived from the frequency relationship, which is typical for the order of harmonic distortion considered. For instance, for third harmonic intermodulation interferences $f_0=2f_1\pm f_2$, in which f_0 is the carrier frequency of the wanted channel and in which f_1 and f_2 are the carrier frequencies of third order harmonic interfering radio-channels. An intermodulation interference indicative signal is generated when the signal level of wanted radio-channel becomes smaller than the signal level of one of the interfering channels.

[0003] However, to measure the signal level of an interfering channel, the receiver has to interrupt its correct tuning position, in which a wanted radio-signal is received, and to switch over its tuning to the interfering channels. To avoid the switching of the tuning and the reception of interfering signals during level detection from being heard, the receiver has to be muted. To come to a reliable level detection, the period during which the receiver has to stay tuned to said interfering channel, has to exceed a certain level detection time constant, therewith defining a minimum duration to the mute period. Furthermore, especially when the receiver is used in a fast changing environment, e.g. when being mounted on a moving object such as an automobile, the interference channel level check has to be carried out rather frequently, therewith putting a minimum repetition putting frequency to the mute actions. This all strongly deteriorates the overall receiver performance.

[0004] It is a first object of the present invention to provide a receiver comprising intermodulation interference detection means for detecting intermodulation interference caused by non-linearities in the gain of analog signals when being processed, e.g. in amplifiers, modulators, correlators, etc. and occurring within the bandwidth of a wanted radio channel, which is more accurate and alert than the one disclosed in the above referenced US patent.

[0005] A second object of the invention is to provide

intermodulation interference detection means which do not necessitate to interrupt processing of the useful radio signal.

[0006] A receiver of the above referred type according to the invention is therefore characterized in that an output of the analog signal processing means is coupled through frequency selective means for channel selection and level detecting means to a first terminal of said intermodulation interference detection means, a signal generator for generating a gain modulation signal being coupled to a gain control terminal of said analog signal processing means as well as to a second terminal of the intermodulation interference detection means, the intermodulation interference detection means comprising a correlator supplying an intermodulation interference indicative signal at a detection of differences in relative amplitude variations of the output signal of the level detecting means at the first terminal of the intermodulation interference detection means on the one hand and the gain modulation signal at the second terminal of the intermodulation interference detection means on the other hand.

[0007] A variation of the gain of the analog signal processing means will cause the level of the useful signal to vary linearly with said gain variation, whereas intermodulation interference components will vary to a second or higher order with said gain variation. According to the invention, this difference in gain variation is used to detect whether the output signal level of the analog signal processing means is predominated by the useful signal or by intermodulation interferences. The occurrence of a signal level at the output of the analog signal processing means varying disproportionately, i.e. to a second or higher order, with a gain variation of the analog signal processing means, is accurately detected in the correlator, becoming apparent in the generation of an intermodulation interference indicative signal. Furthermore, the intermodulation interference detection according to the invention is applied during correct tuning at a wanted radio-signal, which, in contrast to the above mentioned known method of intermodulation interference detection, does not require to change tuning and mute the receiver.

[0008] A preferred embodiment of a receiver according to the invention is characterized in that the signal generator is coupled through inverting means to a modulation signal input terminal of an amplitude modulator following said RF amplifying means.

[0009] This measure neutralizes modulation of said signal output level with the gain control signal therewith preventing the effect of the gain variation on the signal output level of the analog signal processing means from becoming noticeable in the useful signal.

[0010] Preferably, a delay circuit is coupled between the signal generator and said modulation signal input terminal of the amplitude modulator to compensate for the signal delay occurring in the amplifying means.

[0011] Another preferred embodiment of a receiver

according to the invention is characterised in that the signal generator comprises amplitude modulation means to modulate the amplitude of said gain control signal with a bandwidth limited pseudo random modulation signal.

[0012] By using a bandwidth limited pseudo random modulation signal for the gain control signal the amplitude modulation signal energy is spread over a wide frequency range therewith preventing said signal energy from reaching a hearing threshold level at one or more specific frequencies.

[0013] A further suppression of the amplitude modulation signal energy is achieved by limiting the amplitude of the gain modulation signal to 2 dB.

[0014] For a cost effective realisation the amplifying means preferably comprise an controllable attenuator preceding a broadband amplifying stage coupled to the gain control terminal of the amplifying means providing a signal attenuation varying with said gain control signal.

[0015] In a further preferred embodiment the frequency selective means for channel selection comprise a mixing stage followed by an IF circuit for IF channel selection, an output of said IF circuit being coupled through said level detecting means to the first terminal of the correlator.

[0016] In another preferred embodiment the intermodulation interference indicative signal is visualised by supplying the same to a level indicator coupled to display means for displaying the level of the intermodulation interference indicative signal, allowing the user to take adequate measures, e.g. change tuning or lower the receiver gain.

[0017] An automatic change in tuning is obtained by the use of a tuning oscillator supplying a local oscillator signal to the mixing stage being controlled by said processing means to change in frequency when the intermodulation interference indicative signal exceeds a threshold value.

[0018] The above and other objects, features and advantages of the present invention will become more apparent from the following description referring to the accompanying drawings, in which a preferred embodiment of the present invention is shown by way of illustrative example.

[0019] Figures 1a and 1b are graphical diagrams illustrating the phenomenon of intermodulation interference.

Figure 2 shows the basic concept of intermodulation interference detection according to the invention.

Figure 3 is a block diagram of a receiver according to a preferred embodiment of the present invention.

[0020] Figures 1a and 1 b are diagrams illustrating the phenomenon of intermodulation interference, in which Figure 1a shows the differences in amplitude increase between a wanted first order signal s and unwanted second and third order intermodulation products d2 and d3, respectively, as a function of the fieldstrength of the received RF frequency band. These higher order inter-

modulation products are caused by intermodulation interference between all RF carrier signals within the received RF frequency due to non-linearities in the signal processing within (the amplifiers, mixer and demodulator) of a receiver. Where wanted RF signals vary proportionally with variations in the reception fieldstrength, said higher order harmonic distortion components vary non linearly (i.e. to their order of harmonic distortion) with variations in the reception fieldstrength.

5 10 Suppose RF carrier signals f1 and f2 simultaneously occur within the received RF frequency band as shown in Figure 1b. Then intermodulation products occurring nearest to those carrier frequencies f1 and f2, are a second order intermodulation products d2 occurring at f1±f2, and two third order harmonic distortion components d3 occurring at respectively 2f1±f2 and 2f2±f1. Because there are many RF carrier frequencies, it will be clear that the occurrence of unwanted higher order harmonic distortion components within the received RF signal band is in practise unavoidable. The invention, however, makes use of the above difference in amplitude increase between the wanted RF signal and the unwanted higher order intermodulation products to distinguish between wanted signals and unwanted intermodulation products.

20 25 [0021] Figure 2 shows the basic concept of a detection according to the invention of intermodulation interference occurring in analog signal processing means ASP, which may comprise various receiver circuitry as will be explained in more detail with reference to Figure 3. The analog signal processing means ASP are gain controlled by means of a gain modulation signal supplied from a signal generator 10 and modulating the amplitude of an RF input signal with an incremental amplitude variation of δ x dB. The output signal of the analog signal processing means ASP is channel selected and its level within the selected bandwidth is being measured and supplied to a first input of a correlator 12. A second input of the correlator 12 is supplied by the signal generator 10 with the gain modulation signal. The correlator 12 provides an output signal at the occurrence of differences in amplitude variation between the two signals at its first and second input terminal and increasing in amplitude at an increase of said differences. The greater said differences, the more dominant intermodulation products 2 δ x, 3 δ x, occur at the second input terminal of the correlator 12. The output signal of the correlator 12 is therewith indicative for the intermodulation interference occurring in the analog signal processing means ASP.

30 35 40 45 50 [0022] Figure 3 shows a block diagram of a receiver according to a preferred embodiment of the present invention comprising analog signal processing means 1-8 having subsequently connected to antenna means A, an RF input stage 1; controllable RF amplifier means 2, 3, comprising a controllable attenuator 2 and an RF amplifier 3; frequency conversion and selective means 4, comprising a mixer 5 coupled to an IF filter 7, the mixer

5 being supplied with a local oscillator signal from a tunable local oscillator 6; an amplitude modulator 8 followed by IF and audio signal processing means 9. In these analog signal processing means 1-9 the RF receiver input frequency band comprising a wanted RF signal modulated on an RF carrier frequency is being supplied from the antenna means A to the RF input stage 1, comprising a passive, selective matching network, and subsequently broadband amplified in the controllable RF amplifier means 2, 3. An RF to IF conversion of the wanted RF signal is provided in the mixer 5 by mixing the wanted RF signal with a local oscillator signal deviating in frequency from the carrier frequency of the wanted RF signal over a frequency distance equal to the IF frequency. An IF selection of the so converted IF signal is provided in the IF filter 7. Further processing of the IF signal into an audiosignal suitable to be reproduced in audible form is being provided in the IF and audio signal processing means 9. The processing of a wanted RF signal into an audio signal is on itself known and doesn't need further explanation for a proper understanding of the invention.

[0023] An output of the amplitude modulator 8 is coupled to intermodulation interference detection means 10-15 comprising level detecting means 13, 14 having a level detector 13 followed by a bandpass filter 14. The bandpass filter 14 is to select the amplitude modulation signal of the output of the level detector 13 and to block DC components eventually occurring therein. An output of the bandpass filter 14 is coupled to a first terminal of a correlator 12. The intermodulation interference detection means 10-15 also comprise a signal generator 10 for generating a gain modulation signal being coupled to a gain control terminal of the controllable attenuator 2 of said controllable RF amplifier means 2, 3 as well as through a delay circuit 11 to a second terminal of the correlator 12. The delay circuit provides a signal delay corresponding to the signal delay occurring in the circuitry 2-4, 8 of the analog signal processing means 1-9. An output of the correlator 12 is coupled through a low-pass filter 15 to an output terminal of the intermodulation interference detection means 10-15 for supplying thereto an intermodulation interference indicative signal.

[0024] According to the invention the gain modulation signal is used to vary the attenuation of the attenuator 2 and therewith the overall RF signal gain of the controllable RF amplifier means 2, 3. The amplitude variations of the RF signal at the output of the RF broadband amplifier 4 resulting therefrom pass through the mixer 5 and IF filter 7 to an IF signal input of the amplitude modulator 8. In amplitude modulator 8 the IF signal is being varied in amplitude with the inverse gain modulation signal to remove the first order amplitude variations resulting from the gain modulation in the controllable RF amplifier means 2, 3. This is achieved by supplying the gain modulation signal from the signal generator 10 through a delay circuit 11, compensating for the signal delay occurring in the signal path including circuits 2-4, 8, and a

signal inverter 16 providing an inversion of the gain modulation signal, to a control terminal of the amplitude modulator 8. If the signal level at the output of the IF filter 7, i.e. after being processed (i.e. broadband amplified, frequency converted and selected) in the analog signal processing means 1-8 is predominantly determined by the wanted, i.e. first order, signal, then the amplitude variations caused by the gain modulation signal in the controllable RF amplifier means 2, 3 are completely compensated by the inverse modulation of the IF signal level occurring in the amplitude modulator 8. The level detecting means 13, 14 then provide a zero level signal to the first terminal of the correlator 12. The correlator 12 may in practise be formed by a signal multiplier, which in the given situation causes a zero level output signal to occur, which passes through the lowpass filter

15 to the output of the intermodulation interference detection means 10-15.

[0025] However, if the signal level at the output of the IF filter 7 is predominantly determined by unwanted higher order intermodulation products, then the amplitude variations caused by the gain modulation signal in the controllable RF amplifier means 2, 3 of these unwanted higher order intermodulation products are not completely compensated by the inverse modulation of the IF signal level occurring in the amplitude modulator 8. The remaining higher order amplitude variations are being detected in the level detector 13, selected in the bandpass filter 14, and supplied to the first terminal of the correlator 12. In the correlator 12 these remaining higher order amplitude variations are compared (or multiplied) with the gain modulation signal resulting in a DC signal having a signal level varying with the magnitude of said remaining order amplitude variations. After being selected in the lowpass filter 15, this DC signal is indicative for the overall level of the intermodulation products occurring in the IF signal at the output of the IF filter 7. The intermodulation interference indicative signal at the output terminal of the intermodulation interference detection means 10-15 is used in the receiver of Figure 2 to initiate a change of the tuning frequency of the receiver to another wanted RF carrier frequency. This is achieved by a tuning control signal generator 17 coupled to the output terminal of the intermodulation interference detection means 10-15 supplying a tuning control voltage to the local tuning oscillator 6 when the intermodulation interference indicative signal increases beyond a properly chosen threshold value. The tuning control signal generator 17 may comprise (not shown)

50 RDS signal processing capabilities to allow the receiver to automatically switch tuning to e.g. an alternative frequency (so-called AF switching).

[0026] It may also be possible to display the intermodulation interference indicative signal in a numerical or analog intermodulation interference indicator (not shown) indicating quantitatively the overall level of intermodulation products in the received RF signal.

[0027] To make the remaining amplitude variations af-

ter the compensation in the modulation is less noticeable in the reproduced audiosignal, the signal generator 10 may be provided with amplitude modulation means (not shown) to modulate the amplitude of said gain control signal with a bandwidth limited pseudo random modulation signal. The use of such pseudo random modulation signal results in a spectral energy spread of the modulation reducing strongly the audibility of said remaining amplitude variations.

[0028] A further reduction in the audibility of amplitude variations is achieved by limiting the relative amplitude variations caused by the gain modulation signal to approximately 2 dB.

[0029] The invention is not limited to the forms of implementation as described above. For instance, the measures for reducing the audibility of the gain variation may allow to dispense with the amplitude modulator 8. Furthermore dependent on the required accuracy in the detection of intermodulation interference bandpass filter 14 and/or low pass filter 15 may be dispensed with.

Claims

1. Receiver comprising analog signal processing means and intermodulation interference detection means for detecting intermodulation interference occurring in said analog signal processing means, characterized in that an output of the analog signal processing means is coupled through frequency selective means for channel selection and level detecting means to a first terminal of said intermodulation interference detection means, a signal generator for generating a gain modulation signal being coupled to a gain control terminal of said analog signal processing means as well as to a second terminal of the intermodulation interference detection means, the intermodulation interference detection means comprising a correlator supplying an intermodulation interference indicative signal at a detection of differences in relative amplitude variations of the output signal of the level detecting means at the first terminal of the intermodulation interference detection means on the one hand and the gain modulation signal at the second terminal of the intermodulation interference detection means on the other hand.

2. Receiver according to claim 1, characterized in that the signal generator is coupled through inverting means to a modulation signal input terminal of an amplitude modulator following said RF amplifying means.

3. Receiver according to claim 2, characterized by a delay circuit coupled between the signal generator and said modulation signal input terminal of the amplitude modulator.

4. Receiver according to claim 1, characterized in that the signal generator comprises amplitude modulation means to modulate the amplitude of said gain control signal with a bandwidth limited pseudo random modulation signal.

5. Receiver according to claims 1 and 4, characterized in that the amplitude of the gain modulation signal is smaller than 2 dB.

10 6. Receiver according to one of claims 1 to 5, characterized in that the amplifying means comprise an controllable attenuator preceding a broadband amplifying stage coupled to the gain control terminal of the amplifying means providing a signal attenuation varying with said gain control signal

15 7. Receiver according to one of claims 1 to 6, characterized in that the frequency selective means for channel selection comprise a mixing stage followed by an IF circuit for IF channel selection, an output of said IF circuit being coupled through said level detecting means to the first terminal of the correlator.

20 8. Receiver according to one of claims 1 to 7, characterized in that said processing means comprise a level indicator coupled to display means for displaying the level of the intermodulation interference indicative signal.

25 9. Receiver according to claim 7, characterized by a tuning oscillator supplying a local oscillator signal to the mixing stage being controlled by said processing means to change in frequency when the intermodulation interference indicative signal exceeds a threshold value.

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EUROPEAN SEARCH REPORT

Application Number
EP 99 20 2132

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (InCL7)
A	US 5 428 824 A (KASAI YOSHIHIKO) 27 June 1995 (1995-06-27) * column 3, line 49 - line 61; figure 1 * * column 1, line 62 - line 66 * ---	1,4,7,9	H04B1/10 H03G3/30
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A	US 5 564 094 A (ANDERSON GEORGE C ET AL) 8 October 1996 (1996-10-08) * column 4, line 8 - line 12 *	1,2	
A	EP 0 814 568 A (LUCENT TECHNOLOGIES INC) 29 December 1997 (1997-12-29) * abstract; figure 1 *	1,6	
A	PATENT ABSTRACTS OF JAPAN vol. 1995, no. 09, 31 October 1995 (1995-10-31) & JP 07 143025 A (CLARION CO LTD), 2 June 1995 (1995-06-02) * abstract *	1,7	TECHNICAL FIELDS SEARCHED (InCL7)
A	PAWLAK J: "A TUTORIAL ON INTERMODULATION DISTORTION AND NON-LINEARITY IN RF SYSTEMS" RF DESIGN, US, CARDIFF PUBLISHING CO, ENGLEWOOD, CO, vol. 19, no. 2, page 71-74 XP000557686 ISSN: 0163-321X -----		H04B H03G
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	24 November 1999	Peeters, M	
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**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 99 20 2132

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
 The members are as contained in the European Patent Office EDP file on
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